Slovenská technická univerzita v Bratislave Fakulta informatiky a informačných technológií

Ilkovičova 2, 842 16, Bratislava 4

Tímový projekt



Export úloh z nástroja JIRA

Vedúci projektu: doc. Ing. Tibor Krajčovič, PhD.

Spolupráca: Ing. Lukáš Ondriga, Kistler Bratislava, s.r.o.

Názov tímu: TEST.IOT

Členovia tímu: Bc. Tomáš Bujna

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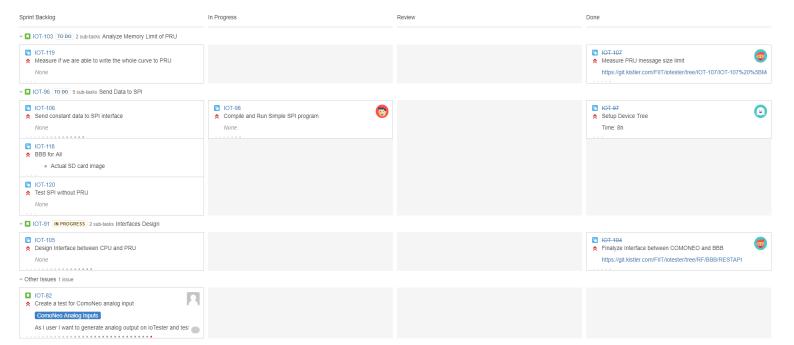
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Akademický rok: 2018/2019



	leeuo kou	Issue Tune	Status	Accionos	Perceiation	Enic Link	Enic Namo	Corint 1	Carlat 2	Carlat 2	Carlat 4	Cordat E	Sprint 6	Corint 7	Stony Boints	Tack type
Summary	Issue key	Issue Type	Status	Assignee	Description	Epic Link	Epic Name	Sprint 1	Sprint 2	Sprint 3	Sprint 4	Sprint 5	Sprint 6	Sprint 7	Story Points	Task type
Project goal	IOT-78	Group	Group		The goal of the porject is to enable automatic testing of measuring devices. For this purpose it is necessary to develop a device able to generate various analog and digital signals which will simulate sensors and device states.											
Configuration of various devices	IOT-73	Group	Group		REST API should not be ComoNeo specific. It should be possible to use the same data model for other devices.											
Robot Framework Tests	IOT-70	Group	Group		Examples of robot framework tests demonstrates the functionality of IoTester.											
Configuration of analog signals	IOT-72	Group	Group		It is possible to configure analog signals over REST API.											
Environment	IOT-71	Group	Group		First prototype of the device is used to test ComoNeo:Â [https://www.kistler.com/en/applications/industrial-process- control/plastic-process-monitoring/injection-molding-process-											
REST API	IOT-75	Group	Group		control/process-monitoring-with-comoneo/] On the basis of ComoNeo analysis create a REST API interface.											
Configuration of digital signals	IOT-77	Group	Group		It is possible to set digital input signals over REST API.											
Design Document how to	IOT-74	Group	Group													
use IoTester for devices other than ComoNeo	IOT-76	Group	Group													
High level architecture	IOT-65	Group	Group		Architecture document contains high level view on PRU, ARM, beaglebone, robot framework and ComoNeo relations. A											
IoTester architecture Architecture	IOT-69	Group	Group		Architecture of the IoTester software is documented.											
document Robot Framework	IOT-68	Group	Group													
tests	IOT-67	Group	Group		The goal of the implementation is to provide several working automated											
Implementation	IOT-62	Group	Group		tests of the ComoNeo device. Harware consists of reusable part and device specific part (e.g. ComoNeo											
Hardware Housing	IOT-63 IOT-61	Group Group	Group		connectors). 3D printer housing modelsÅ is designed.											
Tests integration	IOT-60															
into continuous integration system	101-00	Group	Group													
Document how to use IoTester for ComoNeo	IOT-64	Group	Group	L					L							
Robot framework integration	ЮТ-66	Group	Group													
Documentation	IOT-56	Group	Group		REST API is documented. Documentation contains description how to use the interface for different devices (not Lukáš Ondrigay for ComoNeo).											
Software	IOT-59	Group	Group		and the color of the color of the continued).											
Project goal Test examples	IOT-57	Group	Group													
Implementation IoTester Implementation	IOT-58	Group	Group													
Design Interface between CPU and	IOT-105	Sub-task	To Do								IOT Sprint 4	IOT Sprint	IOT Sprint	IOT Sprint		
PRU					As I user I want to generate analog output on IoTester and test the							-	_			
					behaviour of ComoNeo firmware.											
Create a test for ComoNeo analog	IOT-82	Story	To Do	Marián Ján	Acceptance criteria: * Test sets the measurement start of the ComoNeo to a pin connected to loTester	IOT-36								IOT Sprint		
input	101-82	Story	10 00	Franko	* Test sets the analog output values to the IoTester (e.g. in 10	101-36								7		
					secondsÅ sets 10 different values) * Test starts the measurement with digital output of IoTester											
Test SPI without					* Test checks the values using cursor in ComoNeo web application (see the attachment)								IOT Sprint	IOT Sprint		
PRU	IOT-120	Sub-task	To Do										6	7		
Measure if we are able to write the whole curve to PRU	IOT-119	Sub-task	To Do										IOT Sprint 6	IOT Sprint 7		
BBB for All																
	IOT-118	Sub-task	To Do		* Actual SD card image * Code composer								IOT Sprint	IOT Sprint		
	IOT-118	Sub-task	To Do		* Code composer * Connect to BBB								IOT Sprint 6	IOT Sprint 7		
	ЮТ-118	Sub-task	To Do		* Code composer * Connect to BBB Time: 8h											
Saturi Davica Trae				Bacticlau Knuář	Code composer Connect to 888 Time: 8h Setup Device Tree									7		
Setup Device Tree	IOT-118	Sub-task Sub-task	To Do	Rastislav Kováč	* Code composer * Connect to BBB Time: 8h								6	7		
Setup Device Tree				Rastislav Kováč	* Code composer *Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03								6 IOT Sprint	7 IOT Sprint		
Setup Device Tree					*Code composer *Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGX3ZH075/pins.png] A As a developer of loTester I need a design of the communication message								6 IOT Sprint	7 IOT Sprint		
Setup Device Tree					*Code composer *Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZRIHLDT-FGX32H075/pins.png] Å As a developer of IoTester I need a design of the communication message between PRU and CPU.								6 IOT Sprint	7 IOT Sprint		
Setup Device Tree			Closed		*Code composer *Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HLDT-FGX3ZH075/pins.png] A As a developer of loTester I need a design of the communication message	ют-36					IOT Sprint 4	IOT Sprint	6 IOT Sprint	7 IOT Sprint 7	13.0	
	ЮТ-97	Sub-task	Closed		*Code composer *Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files.pri/TCZR1HLDT-FGK3ZH075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance oriteria:	ЮТ-36					IOT Sprint 4		IOT Sprint 6	7 IOT Sprint 7	13.0	
	ЮТ-97	Sub-task	Closed		*Code composer *Connect to 888 Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result] https://files.slack.com/files-pir/TCZR1HLDT-FGX32H075/pins.png] Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance orteria: **message visit support all digital outputs and analog outputs usable on	ЮТ-36					IOT Sprint 4		IOT Sprint 6	7 IOT Sprint 7	13.0	
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Interfaces Design Analyze Memory Limit of PRU	IOT-91	Story Story	Closed In Progress To Do		*Connect to BBB Time: 8h Setup Device Tree Set pins: 190/194/188/19c to mode 0x03 Result https://files.slack.com/files-pri/TCZR1HLDT-FGX32H075/pins.png Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acegstance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU, 1) **a discounseration of the message - will contain reasoning. **the basic idea how to create this message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system Acceptance criteria: **messure PRU message size limit **measure PRU message size limit **measure in we are able to write the whole curve to PRU As a developer of loTester I need to send a simple message to SPI interface **Acceptance criteria: **acceptance criteria: **prepare a simple program to work with SPI interface - the program is compilable and passible to load into with wRU **enabled SPI and SPIOs which are necessary to control DAC in the device tree **send simply message to SPI interface (possible to measure it by an oscilloscope) As a user of loTester I need the documentation of REST API to be able to use this interface. **REST API is not ComoNeo specific **REST API is not ComoNeo specific **BEST API is not ComoNeo specific **Documentation of REST APIÄ	ЮТ-36					10T Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	IOT Sprint 7 IOT Sprint 7	5.0	
Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-91 IOT-103	Sub-task Story Story	Closed In Progress To Do		*Code composer *Comect to BBB Time: Bh Setup Device Tree Set pins 390;194/158/195 to mode 0x03 Result https://files.slack.com/files-pri/TCZR1HLDT-FGX32H075/pins.png Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance or Iteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) ***message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message are will support all digital outputs and analog outputs of PRU message size limit **message villes are able to write the whole curve to PRU **Acceptance criteria: ***prepare a simple program to work with SPI interface - the program is compilable and possible to load into PRU ***enabled SPI and GPIOs with are necessary to control DAC in the device tree of lotester in lead to send a simple message in the device tree of lotester in lead the documentation of REST API to be able to use this interface. **Acceptance criteria: *****RST API and is not ComoNeo specific ******RST API and all and analog outputs of loTester **Documentation of REST APIA Hint: Analyse the data used in ComoNeo software simulator: [https://git.kister.com/comong/comong-	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	IOT Sprint 7 IOT Sprint 7	5.0	
Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-91 IOT-103	Sub-task Story Story	Closed In Progress To Do		*Code composer *Comect to BBB Time: 8h Setup Device Tree Set pins 390;194/158/19c to mode 0x03 Result https://files.slack.com/files-pri/TCZR1HLDT-FGX32H075/pins.png Å Ac a developer of loTester i need a design of the communication message between PRU and CPU. Acceptance oriteria: *message should be easy to use for PRU (no parsing, no caching in PRU,) **message bround be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **message should be easy to use for PRU (no parsing, no caching in PRU,) **Acceptance oriteria: **message in CPU is described Acceptance oriteria: **message read to the stage in CPU is described Acceptance oriteria: **message read to the stage in CPU is described Acceptance oriteria: **message in CPU is described As a developer of loTester I need to measure the size limit of PRU message system **Acceptance oriteria: **neasure FW usersage size limit **neasure GW usersage in CPU is described As a developer of loTester I need to send a simple message to SPI interface **Acceptance oriteria: **prepare a simple program to work with SPI interface - the program is compilable and possible to load into PRU **enabled SPI and GPIOs which are necessary to control DAC in the device tree **end singly message to SPI interface (possible to measure it by an oxidilizacpe) As a user of loTester I need the documentation of REST API to be able to use this interface. **end singly message to SPI interface oriteria: **EST API is not ComoNeo specific **BST API while is the complex oriteria is mulator: https://git.kider.com/comong/comong-sortware/pree/master/Core/lib/Fpgp/Simulator	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	IOT Sprint 7 IOT Sprint 7	5.0	
Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-91 IOT-103	Sub-task Story Story	Closed In Progress To Do		*Code composer *Comect to BBB Time: Bh Setup Device Tree Set pins 390;194/158/195 to mode 0x03 Result https://files.slack.com/files-pri/TCZR1HLDT-FGX32H075/pins.png Å As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance or Iteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) ***message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message will support all digital outputs and analog outputs usable on lotester **message are will support all digital outputs and analog outputs of PRU message size limit **message villes are able to write the whole curve to PRU **Acceptance criteria: ***prepare a simple program to work with SPI interface - the program is compilable and possible to load into PRU ***enabled SPI and GPIOs with are necessary to control DAC in the device tree of lotester in lead to send a simple message in the device tree of lotester in lead the documentation of REST API to be able to use this interface. **Acceptance criteria: *****RST API and is not ComoNeo specific ******RST API and all and analog outputs of loTester **Documentation of REST APIA Hint: Analyse the data used in ComoNeo software simulator: [https://git.kister.com/comong/comong-	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	IOT Sprint 7 IOT Sprint 7	5.0	
Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-91 IOT-103	Sub-task Story Story	Closed In Progress To Do		*Connect to BBB Time: 8h Setup Device Tree Set pins: 190/194/158/19c to mode 0x03 Result https://files.stack.com/files.pri/TCZR1HLDT-FGX32H075/pins.png Å As a developer of loffseter i need a design of the communication message between PRU and CPU. Acceptance criteria: **message should be easy to use for PRU (no parsing, no caching in PRU,) **acceptance criteria: **decumentation of the message-will contain reasoning **the basic idea how to create this message in CPU is described As a developer of loffseter i need to measure the size limit of PRU message system **Acceptance criteria: **measure FPU message size limit **measure FPU message size limit **measure five are able to write the whole curve to PRU As a developer of loffseter i need to send a simple message to SPI interface **Acceptance criteria: **prepare a simple program to work with SPI Interface - the program is compliable and possible to load into RRU **enabled SPI and GPIOs which are necessary to control DAC in the device tree **and simply message to SPI interface (possible to measure it by an oscilloscope) As a user of loffseter i need the documentation of REST AP1 to be able to use this interface **enablishing his incolloscope specific **REST AP1 is not ComoNeo specific **REST AP1 is not comoNeo specific **REST AP1 is not comoNeo specific **NEST AP1 is not comoNeo software simulator: **Inters./*git Listleter com/comong/comong-software/tree/master/Core/lib/Fpga/Simulator} Various configuration of software simulator: https://git.Listleter.com/comong/comong-software/ree/master/Core/lib/Fpga/Simulator} **Various configuration of software simulator are available here in ApplicationFiles/Simulator folders: https://git.Listleter.com/comong/comong-software/ree/master/Core/lib/Fpga/Simulator}	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	IOT Sprint 7 IOT Sprint 7	5.0	
Interfaces Design Analyze Memory Limit of PRU Send Data to SPI	IOT-91 IOT-103	Sub-task Story Story	Closed In Progress To Do		*Connect to BBB Time: 8h Setup Device Tree Set pins 190/194/198/19c to mode 0x03 [Result https://files.slack.com/files-pri/TCZR1HiLDT-FGK3ZH075/pins.png A As a developer of loTester I need a design of the communication message between PRU and CPU. Acceptance criteria: * message should be easy to use for PRU (no parsing, no caching in PRU, ** message should be easy to use for PRU (no parsing, no caching in PRU, ** message should be easy to use for PRU (no parsing, no caching in PRU, ** message should be easy to use for PRU (no parsing, no caching in PRU, ** message should be easy to use for PRU (no parsing, no caching in PRU, ** message should be easy to use for PRU (no parsing, no caching in PRU, ** message should be easy to use for PRU (no parsing, no caching in PRU, ** the basic lides how to create this message in CPU is described ** Acceptance criteria: ** measure PRU message size limit ** measure PRU message size limit ** measure PRU message size limit ** acceptance criteria: ** acceptance criteria: ** acreater a simple program to work with SPI interface - the program is compilable and possible to load into PRU ** acreater a simple program to work with SPI interface - the program is compilable and possible to load into PRU ** acreater a simple program to work with SPI interface - the program is compilable and possible to load into PRU ** anabled SPI and GPIOs which are necessary to control DAC in the device tree ** send simply message to SPI interface (possible to measure it by an occilioscope) As a user of to Tester I need the documentation of REST API to be able to use this interface. ** REST API is not ComoNeo specific ** REST API as TOC ComoNeo specific ** REST API is not ComoNeo software simulator: [https://gib.kister.com/comong/comong- townware/free/master/Corc/filb/Figa/Simulator] Various configurations of software simulator are available here in ApplicationFiles/Simulator folders:	107-36					IOT Sprint 4		6 IOT Sprint 6 IOT Sprint 6 IOT Sprint 6	IOT Sprint 7 IOT Sprint 7	5.0	

Property Company Property Co					1												
Part						As a user of IoTester I want to be able to set the analog and digital outputs.											
Marchane	REST API for analog					* IoTester Rest API provides a call which allows to set digital and analog											
Marchane		IOT-83	Story	To Do			IOT-36										
Part																	
Part																	
Part	measurement with	IOT-117	Story	To Do			IOT-36										
Part	one curve					* the curve contains 100 points * the curve is displayed on ComoNeo											
Part						As a user I want to be able to test an analog output on IoTester to be able to test analog input of ComoNeo.											
Part		IOT-36	Epic	To Do		* test in robot framework:		ComoNeo Analog Inputs									
Part						** checks if the signal was measured by ComoNeo											
Part	Enable multiple																
Part		IOT-86	Story	To Do		* Rest API is extended so that it allows configuration of all digital and	IOT-40										
Part						* RTU executes the configuration according defined timing											
Part						As a ComoNeo tester I want to be able to take the data for ComoNeo simulator and configure with the IoTester											
Part						Acceptance criteria: * Robot framework keyword which will load configuration from ComoNeo											
Part																	
Processor Proc																	
Part		IOT-85	Story	To Do		software/tree/master/Core/lib/Fpga/Simulator]	IOT-40										
Part	data conversion	10103	Story	1000			10140										
Part						3.0/Testing/RestApi-Robot/Setups/2molds/ApplicationFiles/Simulator]											
Marche March						software/tree/master/Testing/RestApi-											
Marie						Robot/Setups/8c1p/ApplicationFiles/Simulator]											
Marie						Â											
Process Proc		IOT-49	Story	In Progress	Miroslav Sabo	that we can use it as a guideline for creating final design of our new	IOT-1				IOT Sprint 3					8.0	
Process	Tor Board Design																
Section 1982 1982																	
No. 1.0		IOT-92	Story	In Progress	Rastislav Kováč	sets one value to the DAC converter	IOT-36					IOT Sprint 4	5			21.0	
Part						* Robot Framework test checks if the cycle started and checks if the value											
Legislation of the control of the co					Lukáš Ondriga				IOT Sprint 1	IOT Sprint 2							
	Deployment	IOT-89	Epic	To Do		As a user I want to have access to the IoTester REST API after boot.		Deployment									
Marie		IOT-116	Story	To Do		Acceptance criteria:	IOT-89										
Configuration of the Configuration of the Configuration State of the Conf																	
Part	Land POLL and					As a user I want to have the PRU software loaded automaticaly after boot.											
Configura above Configura		IOT-115	Story	To Do		* PRU application is added to Yocto build	IOT-89										
The field of the control of the cont																	
Section of the content of the cont						the correct device tree.											
Marke fields and Marke fields and service from the field of the service of fields and service from the fields and		IOT-114	Story	To Do		* device tree configuration added to Yocto layer	IOT-89										
And finds articles of the control of						(e.g. spi bits have correct mode)											
Comparison of the Comparison	Add flusk into SD																
Author A	card image	IOT-113	Story	To Do		* new layer added to yocto configuration	IOT-89										
State Stat						* image configuration including flusk added * generated image can be load to the sd card and the flusk is installed											
Comparison of the Comparison						As a developer I need to be able to generate new SD card image.											
Marian M		IOT-81	Story	To Do		* Script for building SD card image from existing yocto configuration for TI	IOT-89										
Properties Pro						* Script is available in Git repository											
Prepara Prep		IOT-111	Task	Closed	Stanislav Širka												
Relationing HW More better compactnoors of 11 class of 10 closed Stanislaw Sirks of 10 closed Stanislaw	Trello Prepare													IOT Sprint			
New Informations New Informa	Sprint 6				Journaley SIFR&									6			
Propert Prop	better compactness	IOT-1	Epic	To Do				IoTester Refactoring									
For Spring Residence Color	JIRA Tasks	IOT-112	Task	Closed	Stanislav Širka									6			
Framework test for testing cycle values Framework testing cycle values Framework test for testing cycle values Framework test for testing cycle values Framework testing cycle values Fram		IOT-109	Task	Closed	Stanislav Širka									IOT Sprint 6			
Teaching clienter Teac	Framework test for	IOT-108	Task	In Progress			IOT-36										
between CMOMPR of 17-104 sub-task Closed of Tomáš Bujna https://git.kistler.com//HIT/lotester/tree/RF/BBB/RESTAPI cm.													IOT 5		IOT 5		
Minusers	between COMONEO	IOT-104	Sub-task	Closed	Tomáš Bujna	https://git.kistler.com/FIIT/iotester/tree/RF/BBB/RESTAPI						IOT Sprint 4		6	7		
Understands daisy chain concept		IOT-107	Sub-task	Closed	Tomáš Bujna												
Complete and flow program OT-98 Sub-task In Progress Filip Stary Sub-task In Progres	Understands daisy-	IOT-102	Suh-tack	In Progress	Igor I ahát	and the state of t								IOT Sprint			
Simple SFI program OT-100 Sub-task Blocked ligor Labst Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Understand how the DAC Chip is connected to the TOT-101 Sub-task Blocked Miroslav Sabo Windsiav Sabo Understand how the TOT-101															IOT Sprint		
of DAC Understand how the DAC chip is connected to the 10T-101 Sub-task Blocked Miroslav Sabo	Simple SPI program	IOT-98	Sub-task	In Progress	Filip Starý									6			
DAC chip is OT-101 Sub-task Blocked Miroslav Sabo OT-101 Sub-task Blocked Miroslav Sabo OT-101	Basic understanding of DAC	IOT-100	Sub-task	Blocked	Igor Labát									IOT Sprint 6			
connected to the CONTROL SUCCESS CONTROL SUCCE			eut e i		Address of the Control of the Contro									IOT Sprint			
	connected to the	101-101	Sup-task	Biocked	Mirosiav Sabo												

										1	1					
Analyze Analog Output of DAC	ЮТ-99	Story	To Do		As a developer of loTester I need to have a basic understanding of how to communicate with DAC, how to setup DAC to get desire analog value *analyze how the DAC chip is connected to the board (SPI, GPIOS) - which BBB pins are used to control DAC - as an output prepare a simple sketch of *PM description *analyze how to use *analyze how to use *analyze how to use *analyze bow to use *analyze to the pins of the pins o	ЮТ-36							IOT Sprint 6		5.0	
Send constant data to SPI interface	IOT-106	Sub-task	To Do										IOT Sprint 6	IOT Sprint 7		
Create Project Documentation for	IOT-95	Story	Closed	Stanislav Širka								IOT Sprint			8.0	
ZS Digital Input Test	ЮТ-90	Story	Closed	Stanislav Širka		IOT-2					IOT Sprint 4	5 IOT Sprint			13.0	
Integration Model Architecture						101-2						5			13.0	
for Project	IOT-80	Task	Closed	Stanislav Širka	As a user I want to be able to set digital output from RTU to be able to test					IOT Sprint 3	IOT Sprint 4					documentation
Program for RTUexe Configuration	IOT-9	Story	Closed	Filip Starý	ComoNeo digital input. Acceptance criteria: Running RTU program which sets the digital output of IOTester according	IOT-2					IOT Sprint 4				8.0	
Create First Document for	IOT-93	Story	Closed	Stanislav Širka	configuration from CPU.						IOT Sprint 4				8.0	
Project Close Sprint 3	IOT-93	Task	Closed	Stanislav Širka							IOT Sprint 4				8.0	
Testing digital inputs	IOT-2	Epic	To Do	Statislay Sirka			ComoNeo Digital Inputs				101 Sprint 4					
on ComoNeo			Closed													documentation
Create Team Poster Decide on	IOT-26	Task														documentation
Continuous Server	IOT-22	Task	Closed													
Call Program on RTU from CPU	IOT-19	Task	Closed			IOT-2										implementation
Analyze Communication Between RTU and CPU	IOT-18	Task	Closed			IOT-2										analysis
Analyze RTU Choose Simple	IOT-13	Task Task	Closed			IOT-2										analysis analysis
Program for RTU RTU and Web	IOT-14	Task	Closed			IOT-2										analysis
Server Compatibility	101-14	IdSK	Closed		As a user I want to be able to set digital output from RTU to be able to test	101-2										analysis
Load Program to RTU	IOT-17	Task	Closed	Igor Labát	ComoNeo digital input. Acceptance criteria: Running RTU program which sets the digital output of IOTester according configuration from CPU.	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3						implementation
Methodics	IOT-44	Story	Closed	Stanislav Širka	Create methodic for: **Meeting Documentation **Tasks managment - done *Methodics - done **Code versioning - done *Web - done			IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					3.0	
RTU and CPU Communication	IOT-8	Story	Closed	Filip Starý	As a user I need to configure real time simulation to run various simulations. Acceptance criteria: RTU and CPU prototype is running on Beaglebone Linux console.	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					5.0	
Load RTUexe	IOT-7	Story	Closed	Igor Labát	As a user I need to do a real time simulation to be able to simulate sensor measurements. Acceptance criteria: Loading of the program to the real time unit will be shown on Linux console.	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					13.0	
Jenkins pipeline for installation image	IOT-88	Story	To Do		Consource Acceptance criteria: *Jenkins pipeline which will be trigerred by the change in a glit branch and will compose the lofester firmware	IOT-89										
Create Project Specification	IOT-25	Task	Closed	Lukáš Ondriga												documentation
Analyze, design, implement REST API	IOT-40	Epic	To Do				REST API									
Manage Kistler resources for PCB design	IOT-53	Task	Closed	Lukáš Ondriga						IOT Sprint 3						
Create Document for Tasks	IOT-52	Task	Closed	Stanislav Širka						IOT Sprint 3						documentation
Managment Presentation for	IOT-79	Task	Closed	Stanislav Širka						IOT Sprint 3						
Sprint 3 End Close Sprint 2 Create Document	IOT-50	Task	Closed	Stanislav Širka						IOT Sprint 3						
for Jira Changes REST API Prototype	IOT-10	Task	Closed	Stanislav Širka Tomáš Bujna	As a user of IOTester I want to have interface to set the Como digital input to be able to configure IOTester. Acceptance criteria: **Comprise or the state of	IOT-2				IOT Sprint 3					3.0	documentation
Robot Framework LIB	IOT-11	Story	Closed	Marián Ján Franko	As a test developer I want to have a library to use IOTester Acceptance criteria: * Pythton module * keywords to set Como digital inputs are implemented HHNTA implementation of the keywords are POST requests to the IOTester POST request is implemented in A http://jra.kistler.com/browse/IOT-10	IOT-2				IOT Sprint 3					5.0	
Port IoTester	IOT-54	Task	Closed	Lukáš Ondriga						IOT Sprint 3						
specification to Jira Decide on Our	IOT-29	Task	To Do													
Guidelines Create a Test	IOT-12	Story	Closed	Marián Ján Franko	As a user I want to test the ComoNeo digital input. Acceptance oriteria: Test configures io Test originaria	IOT-2		IOT Sprint 1	IOT Sprint 2	IOT Sprint 3					13.0	
Analyze Board	IOT-3	Story	Closed	Miroslav Sabo	As a hardware engineer I need to analyse the current board to be able to make the final design. Acceptance criteria: Document the current design of the board.	IOT-1		IOT Sprint 1	IOT Sprint 2						8.0	

Close Sprint 1 01-48 Task Closed Stanislav Sirka Create sprint 2. Export tasks from Jira. Create Tasm Websiter 1 01-30 Story Closed Tomás Bujna Closed Stanislav Sirka Sprint skas from Jira. Create Tasm Websiter 1 01-30 Story Closed Tomás Bujna Closed Stanislav Sirka Sprint skas from Jira. Create Tasm Websiter 1 01-30 Story Closed Stanislav Sirka Closed Stanislav Sirka Sprint skas from Jira. Chose Web Server Technology for Linux (NF 5 sprint 1 Story Closed Stanislav Sirka Sprint Sprin											 		
Create Name						Close sprint 1.							
Control Websites Control Web	Close Sprint 1	IOT-48	Task	Closed	Stanislav Širka				IOT Sprint 2				
No. 10 Story Control Sto						Export tasks from Jira.							
March Marc	Create Team	10T 30	Can	Classed	Tomat I During			IOT Coulot 1				0.0	
Choose Web Server Create Methodics Discount Dis	Website	101-30	Story	Ciosea	Tomas Bujna			IOI Sprint 1				8.0	
Technology OT-15 Task Closed Rasislav Kovás R	Print User Stories	IOT-47	Task	Closed	Stanislav Širka			IOT Sprint 1					
Technology OT-15 Task Closed Rasislav Kovás R	Chanca Wah Saruar												
Feethology for Juny Part		IOT-15	Task	Closed	Rastislav Kováč		IOT-2	IOT Sprint 1					
Fechology for Intury (Web Servery Processing Server	reciniology												
Fechology for Intury (Web Servery Processing Server						As a developer I want to coloct frameworks (technologies to be able to write							
Technology for June 10T-6													
Create Methodic for Norther Share Create Methodic for North Share Cr	Tbl					KEST AFT for beaglebone black real time unit configurations.							
Create Methodic of Methodic		IOT-6	Story	Closed	Rastislav Kováč	A	IOT-2	IOT Sprint 1				5.0	
Create Methodic of Methodic	(web server)					Acceptance criteria:							
Create Methodic of Methodic													
Methodic Dictionary Dicti						Document 3 afternatives with pros and cons.							
December	Create Methodic for												
Sample S	Methodics	IOT-45	Task	Closed	Stanislav Širka			IOT Sprint 1					documentation
Add Tasks to Jira 19-20 Task Closed Stanislav Siria Create Sprint - Create Sprint	Document							· ·					
Add Tasks to Ira Closed Sanislav Sirka Closed Clo	Export Data From	IOT 40	Tools	Classed	Chamielau čieles			IOT Carles 1					
Add Tasks to Jira No	Jira - Sprint 1 Start	101-40	Idak	Cioseu	Statilislav Sil Ka			101 Sprint 1					
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Share Google Drive 107-24 Task Closed Sanislav Srive Conference Confe	Add Tacks to lies	IOT 20	Tack	Clored	Stanielau čieka			IOT Sprint 1					
Share Google Drive 17-24 Task Closed Same Google Drive Image: Closed Drive Dri	Add Tasks to Jira	101-20	IdSK	Ciosea	Statilslav Sirka	* Add tasks to Sprint - done		IOI Sprint 1					
Write TP1						* Add task owners - done							
Write TP1	Shara Google Drive	IOT 24	Tack	Clored									
Requirements Vi7-32 Task Closed Stanislav Sirka	Share Google Drive	101-24	Idak	Cioseu									
Negurements	Write TP1	107.33	Taul.	Classed	Canadalan Flate								
Create Faun Claim California Californi	Requirements												
Uddate Freilo 107-31 Talk Closed Stanislav Sirks	Study SCRUM	IOT-33			Stanislav Širka								
Deciration Documents Documents Study Poler Carls Task Closed Stanislav Siria Closed	Create Team Chat	IOT-23	Task	Closed									
Documents 101-22 Task Closed Good of Commentation Documents 107-34 Task Closed Stanislaw Sirka —	Update Trello	IOT-31	Task	Closed	Stanislav Širka								
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